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EXAMINER		ART UNIT	CLASS-SUBCLASS				
nonprovisional	NO	\$1400	\$300		\$1700	01/12/2006	
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE	
TITLE OF INVENTION: IMAGE SYSTEM, SOLID-STATE IMAGING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE USED IN THE IMAGE SYSTEM, AND DIFFERENCE OUTPUT METHOD USED FOR THE IMAGE SYSTEM							
09/927,632	27,632 08/13/2001		Yoshiyuki Matsunaga	21274	212740US-2SRD CONT 8375		
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR	ATTO	RNEY DOCKET NO.	CONFIRMATION NO.	
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